



CERIES

Tohoku University Electro-Related Departments
Global COE Program

Center of Education and Research for Information Electronics Systems

NPC and GCOE Joint Workshop

The 2nd Workshop on Algorithms and Systems for Super High-Speed and High-Precision Digital Signal Processing

NPC Workshop

General Chair: Michitaka Kameyama (Tohoku University)
Secretary: Masahide Abe (Tohoku University)

GCOE Workshop

General Chair: Masayuki Kawamata (Tohoku University)
Secretary: Shunsuke Yamaki (Tohoku University)

- Date: Friday, 20 November 2009 13:30 - 17:30
- Venue: 6-6-05, Aoba, Aramaki, Aoba-ku, Sendai, 980-8579
Seminar Room 204, Research Building No. 2, Electrical, Information
and Physics Engineering, Aobayama Campus, Tohoku University
- Program

Regular Session Chair: Masahide Abe (Tohoku University)

13:30~14:05

"TMR Logic and Its Prospects" Takahiro Hanyu, Tohoku University

14:05~14:40

"High-Accuracy Image Matching Using Phase-Only Correlation and Its Application
— From 3D Vision to Medical Image Processing" Takafumi Aoki, Tohoku University

14:40~15:15

"Prospects for Real-World Intelligent Systems and Heterogeneous
Multi-core Architecture" Masanori Hariyama, Tohoku University

15:15~15:50

"Recent Topics of Digital Filters" Masayuki Kawamata, Tohoku University

15:50~16:00

Break

Invited Lecture Chair: Masayuki Kawamata (Tohoku University)

16:00~17:00

"Interest in Discrete Fourier Transform" Yoshiaki Tadokoro, Toyohashi University
of Technology

18:00~

Banquet